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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/733,686	12/08/2000	Alexander Goldovsky	9	3232

7590 03/10/2004  
Ryan, Mason & Lewis, LLP  
90 Forest Avenue  
Locust Valley, NY 11560

EXAMINER

DO, CHAT C

ART UNIT PAPER NUMBER

2124

DATE MAILED: 03/10/2004

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Please find below and/or attached an Office communication concerning this application or proceeding.

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**Office Action Summary**

Application No.

09/733,686

Applicant(s)

GOLDOVSKY, ALEXANDER

Examiner

Chat C. Do

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 22 January 2004.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-17 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 5-8 is/are allowed.
- 6) ☒ Claim(s) 1-2, 4, 9-17 is/are rejected.
- 7) ☒ Claim(s) 3 is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- |   |   |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)             | 4) <input type="checkbox"/> Interview Summary (PTO-413)                     |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)    | Paper No(s)/Mail Date. _____  |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| Paper No(s)/Mail Date _____   | 6) <input type="checkbox"/> Other: _____                                    |

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### DETAILED ACTION

1. This communication is responsive to Amendment A, filed 1/22/2004.
2. Claims 1-17 are pending in this application. Claims 1, 5-7, and 16-17 are independent claims. In Amendment A, claims 1, 5-8, and 16-17 are amended. This action is made final.

### *Claim Rejections - 35 USC § 102*

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

4. Claims 1-2, 4, 9-10, 12, and 14-17 are rejected under 35 U.S.C. 102(b) as being anticipated by Poon (U.S. 5,257,218).

Re claim 1, Poon discloses in Figures 8-15 an adder comprising: a plurality of computational stages (e.g. row0-row3 in Figure 10 for computing carry-output) each associated with one or more bit positions of the adder (A0-A7 and B0-B7 in Figure 10), the plurality of computational stages including one or more computational stages for generating a sum output signal (500 in Figure 13) and a primary carry-output signal of the adder (any one of C<sub>0</sub>-C<sub>N-2</sub> from 300 in Figure 13); and a flag generation circuit coupled to at least one signal line of at least one of the computational stages and operative to generate an overflow flag (C<sub>N-1</sub> and col. 13 lines 9-12) for the adder, the overflow flag being separate and distinct from the primary carry-output signal (C<sub>N-1</sub> is

separate and distinct from other  $C_0$ - $C_{N-2}$ ), the overflow flag being generated substantially in parallel with generation of at least one of the sum output signal and the primary carry-output signal of the adder (300 and 500 in Figure 13).

Re claim 2, Poon further discloses in Figures 8-15 the adder comprises an n-bit adder (e.g. 1-bit adder in Figure 12) and the sum output signal comprises a final sum bit of the n-bit adder ( $S_0$ - $S_{N-1}$  in 500).

Re claim 4, Poon further discloses in Figures 8-15 the flag generation circuit does not require the primary carry-output signal to generate the overflow flag for the adder (col. 13 lines 9-12).

Re claim 9, Poon further discloses in Figures 8-15 the adder comprises a prefix tree adder having a plurality of prefix trees, each associated with one of the bit positions of the adder and including one or more of the computation stages (Figure 15).

Re claim 10, Poon further discloses in Figures 8-15 the adder comprises a carry-lookahead adder (col. 20 lines 22-24).

Re claim 12, Poon further discloses in Figures 8-15 the adder comprises a ripple adder (col. 1 lines 20-25).

Re claims 14-15, Poon further discloses in Figures 8-15 the adder comprises a radix-2 adder and non-radix-2 adder.

Re claim 16, it is an integrated circuit claim of claim 1. Thus, claim 16 is also rejected under the same rationale in the rejection of rejected claim 1.

Re claim 17, it is a method claim of claim 1. Thus, claim 17 is also rejected under the same rationale in the rejection of rejected claim 1.

***Claim Rejections - 35 USC § 103***

5. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

6. Claims 11 and 13 are rejected under 35 U.S.C. 103(a) as being obvious over Poon (U.S. 5,257,218).

Re claims 11 and 13, Poon does not disclose the adder comprises a carry-skip adder or a carry-save adder. However, the examiner takes an official notice that the carry-skip adder or the carry-save adder is widely used in conventional adder. Therefore, it would have been obvious to a person having ordinary skill in the art at the time the invention is made to have the adder as a carry-skip adder or a carry-save adder because it would enable to generate the carry-bits quickly and relatively low-cost.

***Allowable Subject Matter***

7. Claims 5-8 are allowed.

8. Claim 3 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

***Response to Arguments***

9. Applicant's arguments with respect to claims 1-2, 4, and 9-17 have been considered but are moot in view of the new ground(s) of rejection.

a. The applicant argues in page 8 fourth paragraph that the used of the primary carry-output signal itself  $C_{N-1}$  as an overflow bit in the manner disclosed in Poon fails to meet the limitation cited in claim 1.

The examiner respectfully submits that the claim language does not clearly cite that the primary carry-output signal is  $C_{N-1}$ . Therefore, the examiner considers the primary carry-output signal is any one of  $C_0$ - $C_{N-2}$ . In addition, the cited reference clearly discloses that the carry  $C_{N-1}$  is available at the output as an overflow bit (col. 13 lines 9-10) and distinct from others  $C_0$ - $C_{N-2}$ .

***Conclusion***

10. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event,

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however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Chat C. Do whose telephone number is (703) 305-5655. The examiner can normally be reached on M => F from 7:00 AM to 4:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Chaki Kakali can be reached on (703) 305-9662. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Chat C. Do  
Examiner  
Art Unit 2124

March 3, 2004

*Kakali Chaki*  
**KAKALI CHAKI**  
**SUPERVISORY PATENT EXAMINER**  
**TECHNOLOGY CENTER 2100**